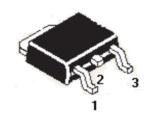




SURFACE MOUNT POSITIVE VOLTAGE REGULATOR



1. Input

2. Ground

3. Output

CL7809DT TO-252 (DPAK) Plastic Package

FEATURES

Maximum output current $(I_{OM}) = 1.5A$

Output Voltage $(V_0) = 9V$

Continuous total dissipation (P_D) = 1.25W (T_a =25°C)

ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

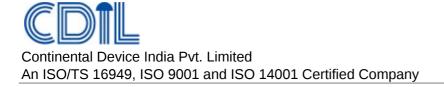
Parameter	Symbol	Value	Unit
Input Voltage	V_{i}	35	V
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	80	°C/W
Operating Junction Teperature Range	T _{OPR}	-25 to 125	°C
Storage Temperature Range	T_{STG}	-65 to 150	°C

ELECTRICAL CHARACTERISTICS AT SPECIFIED VIRTUAL JUNCTION TEPERATURE (V_i =16 V_i),

 I_o =500mA, C_i =0.33 μ F, C_o =0.1 μ F, unless otherwise specified)

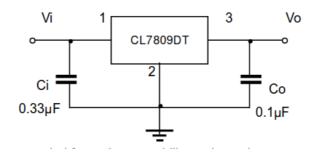
Paramter	Symbol	Test Conditions			Тур	Max	Unit
			25°C	8.65	9	9.35	V
Output Voltage	V _o	11.5V≤V ₁ ≤24V, I ₀ = 5mA-1A, P≤10W	-25 to 125°C	8.55	9	9.45	V
Load Regulation	ΔV _o	I _o = 5mA-1.5A	25°C		12	180	mV
		I _o =250mA-750mA	25°C		4	90	mV
Line Regulation	ΔV _o	11.5V≤V ≤27V	25°C		7	180	mV
		13V≤V,≤19V	25°C		2	90	mV
Quiescent Current	l _q		25°C		4.3	8	mA
Quiescent Current Change	Δl _q	11.5V≤V ≤27V	-25 to 125°C			1	mA
		5mA≤I _o ≤1A	-25 to 125°C			0.5	mA
Output Voltage Drift	$\Delta V_{o}/\Delta T$	I _o =5mA	-25 to 125°C		-1		mV/ºC
Output Noise Voltage	V _N	10Hz≤f≤100kHz	25°C		60		μV/V _o
Ripple Rejection	RR	12V≤V _i ≤22V, f=120Hz	-25 to 125°C	55	70		dB
Dropout Voltage	V _d	I _o =1A	25°C		2		V
Output Resistance	R _o	f=1kHz	25°C		18		mΩ
Short Circuit Current	I _{sc}		25°C		400		mA
Peak Current	l _{pk}		25°C		2.2		А

Pulse Test





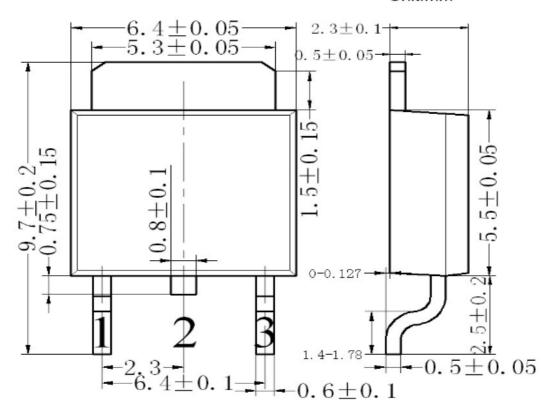
Typical Application



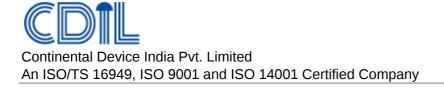
Note: Bypass capicitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.

TO-252 (DPAK) Package Outline and Dimensions

Unit:mm



- 1. Input
- 2. Ground
- 3. Output





Customer Notes:

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

DICLAIMER

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).



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